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## 546680 -- Patent Information

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Title	Substrate cleaning tool, apparatus, and method								
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Priority	<table><tr><td>Country</td><td>Application Number</td><td>Priority Date</td></tr><tr><td></td><td>JP19990211358</td><td>1999/07/26</td></tr></table>			Country	Application Number	Priority Date		JP19990211358	1999/07/26
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Applicant	<table><tr><td>Name</td><td>Country</td><td>Individual/Company</td></tr><tr><td>TOKYO ELECTRON LIMITED</td><td>JP</td><td>Company</td></tr></table>			Name	Country	Individual/Company	TOKYO ELECTRON LIMITED	JP	Company
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TOKYO ELECTRON LIMITED	JP	Company							
Abstract	In a clean apparatus for substrates, such as semiconductor wafers, a permeable core member 68 made of synthetic resin is supplied with distilled water from a distilled water supply path 60 within a head portion 66 of a cleaning tool 24. A planar portion 72 is provided on the lower surface of the core member 68 and a porous resin sheet 69 is attached to the outer surface of the core member 68, to cover it. The cleaning tool 24 is provided with an air bearing cylinder 30 that presses against the surface of a wafer W which is being rotates, and the distilled water supply path 60 is provided within the rod 31. The distilled water supplied from the distilled water supply path 60 permeates through the core member 68 and the porous resin sheet 69 and is sent out of the head 66 so								

that a flowing film of distilled water is formed on the surface of the wafer W. Thus contamination and damage of the substrate is prevented and the shape of the head is not destroyed.